



PRAKASAM ENGINEERING COLLEGE

O.V. Road, Kandukur, Prakasam Dist., A.P., Tel : 08598 222288, 221300

APPROVED BY AICTE, NEW DELHI | AFFILIATED TO JNTU, KAKINADA, UNDER AMMA EDUCATION SOCIETY

Department of Electronics and Communication Engineering

To
The Principal,
Prakasam Engineering College,
kandukur.

01-12-2016
KANDUKUR

Sir,

Sub: Request for conducting a certification course on "VLSI DESIGN FUNDAMENTALS"-Reg.

This is to bring to your kind notice that, we would like to organize a Certification Course program on "VLSI DESIGN FUNDAMENTALS"- scheduled on **26th DEC 2016 TO 17th MAR 2017. On every day evening 4:00pm to 5:00pm** We are happy to inform APSSDC will be conducting this program which is useful for the students of III B.Tech Students.

Thanking you Sir,

Yours sincerely

Approved
[Signature]

HOD-ECE

[Signature]
Head of Department
Department of Electronics and Communication Engineering
Prakasam Engineering College
O.V. Road, Kandukur-523 105
Prakasam Dist. A.P.

PRINCIPAL
PRAKASAM ENGINEERING COLLEGE
KANIGIRI ROAD
KANDUKUR-523 105.
Prakasam Dist, A.P.



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Department of Electronics and Communication Engineering

Date: 05-12-2016

CIRCULAR

All the III B.Tech students are hereby informed that our department is going to conduct 10 Days training program from 26th DEC 2016 TO 17th MAR 2017 on "VLSI DESIGN FUNDAMENTALS" interested students can enrol their names in the department on or before 15-12-2016.

Head of the Department
Department of Electronics and
Communications Engineering
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0.V. Road, KANDUKUR-523 105
PRAKASAM DIST. A.P.
HOD-ECE



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Department of Electronics and Communication Engineering

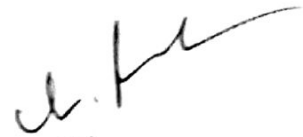
SYLLAB SYLLLABUS:

COMBINATIONAL LOGIC CIRCUITS

SEQUENTIAL LOGIC CIRCUITS

DESIGNING ARITHMETIC BUILDING BLOCKS

IMPLEMENTATION STRATEGIES


Head of the Department
Department of Electronics and
Communication Engineering
Prakasam Engineering College
Kandukur, Prakasam Dist., A.P.

PRAKASAM ENGINEERING COLLEGE
(Affiliated to JNTUK KAKINADA and Approved by AICTE, New Delhi)
KANDUKUR


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VLSI DESIGN FUNDAMENTALS

REGISTERED STUDENTS LIST

S.NO	ROLL NO	NAME
1	15F91A0410	BHARATH KUMAR.D
2	15F91A0426	VENKATA PAVANKALYAN.B
3	15F91A0428	KRISHNAMOHAN.P
4	15F91A0436	MADHAVA REDDY.V
5	15F91A0437	MADHAVA REDDY.Y
6	15F91A0446	NAGESH.V
7	15F91A0452	PRASAD.Y
8	15F91A0453	PRAVALLIKA.CH
9	15F91A0455	PRUDHVI SAI.T
10	15F91A0463	SAI PRATHYUSHA.P
11	15F91A0465	SAI.P
12	15F91A0475	SIVA MEGHANA.M
13	15F91A0476	SIVA NAGA BRAHMA CHARI.P
14	15F91A0479	SRAVANI.K
15	15F91A0488	TEJA.P
16	15F91A0491	VENKATA NAGA DEVAYANI.S
17	15F91A0493	KALYAN.S
18	15F91A0498	VENU.A
19	15F91A04A0	VIJAYA LAKSHMI.P
20	15F91A0401	AFRID.SK
21	15F91A0402	AJANTHA.S
22	15F91A0403	AMULYA.Y
23	15F91A0404	ANIRUDH BABU.P
24	15F91A0405	ANITHA.Y
25	15F91A0406	ANJALIDEVI.A
26	15F91A0407	ANUPAMA.T
27	15F91A0408	ANUSHA.R
28	15F91A0409	BALA MOUNIKA.D
29	15F91A0412	BRAHMA TEJASWANI.N
30	15F91A0413	BRAHMANI.S
31	15F91A0414	CHAANDENI.SD
32	15F91A0416	CHANDRAKALA.M
33	15F91A0417	CHANDRIKA.K
34	15F91A0418	CHOWDARY .R.SANDHYA

35	15F91A0419	DEDEEPIYA.I
36	15F91A0420	DIVYA.P
37	15F91A0421	HAFEEFA BEGUM.MD
38	15F91A0422	JAYA LAKSHMI.D
39	15F91A0423	JWALA SWARNA BHARATHI.R
40	15F91A0424	JYOTHI.K
41	15F91A0425	KALYAN.K
42	15F91A0427	KALYANI.S
43	15F91A0429	KUMAR SAI RAM.K
44	15F91A0431	LAKSHMI MANISHA.K
45	15F91A0432	LAKSHMI PRASANNA.P
46	15F91A0433	LAKSHMI.S
47	15F91A0434	LAVANYA.D
48	15F91A0435	MADEENA.SK
49	15F91A0438	MADHURI.P
50	15F91A0439	MALLESWARI.P
51	15F91A0440	MANASA.M
52	15F91A0441	MANASA.N
53	15F91A0442	MANISHA.M
54	15F91A0443	MEENAKSHI.T
55	15F91A0444	MOHAMMED ABBAS.SK
56	15F91A0445	MOUNIKA.T
57	16F95A0401	JAGARLAMUDI AKHIL
58	16F95A0402	KADIRI BUJJAMMA
59	16F95A0403	SHAIK MASTHAN BASHA


 Head of the Department
 Department of Electronics and
 Communication Engineering



APSSDC

(Andhra Pradesh State Skill Development Corporation)
Department of Skill Development,
Entrepreneurship & Innovation, Government of Andhra Pradesh



Skill Improvement Program (SIP)

on

VLSI Design Fundamentals Course Completion Certificate

This is to certify that Dhanenkula Bharath Kumar from PRAKASAM ENGINEERING COLLEGE has successfully

participated in and completed the course on VLSI Design Fundamentals held from 26th Dec 2016 to 17th March 2017

PRINCIPAL
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KANDAMCHERU, 523 105.
Prakasam District, A.P.

Subbarao Ishank
APSSDC



APSSDC

(Andhra Pradesh State Skill Development Corporation)
Department of Skill Development,
Entrepreneurship & Innovation, Government of Andhra Pradesh



Skill Improvement Program (SIP)

on

**VLSI Design Fundamentals
Course Completion Certificate**

This is to certify that Bhaskaruni Venkata Pavankalyan from PRAKASAM ENGINEERING COLLEGE has successfully

participated in and completed the course on VLSI Design Fundamentals held from 26th Dec 2016 to 17th March 2017

[Signature]

Principal

[Signature]

APSSDC